

REMARKS

The Official Action mailed February 13, 2006, has been received and its contents carefully noted. This response is filed within three months of the mailing date of the Official Action and therefore is believed to be timely without extension of time. Accordingly, the Applicant respectfully submits that this response is being timely filed.

The Applicant notes with appreciation the consideration of the Information Disclosure Statements filed on June 2, 1999; March 31, 2000; June 21, 2000; July 30, 2001; February 15, 2002; April 18, 2002; October 28, 2002; November 21, 2002; January 3, 2003; July 1, 2003; December 16, 2003; and September 22, 2004. A further Information Disclosure Statement was submitted on March 30, 2006, and consideration of this Information Disclosure Statement is respectfully requested.

Claims 1-86 are pending in the present application, of which claims 1, 10, 19, 32, 45 and 68 are independent. Claims 1, 10, 19, 32, 45, 68 and 81-86 have been amended to better recite the features of the present invention. For the reasons set forth in detail below, all claims are believed to be in condition for allowance. Favorable reconsideration is requested.

Paragraph 2 of the Official Action still rejects claims 1-8, 10-17, 19-30, 32-43, 45-56 and 58-80 as obvious based on the combination of U.S. Patent No. 5,250,931 to Misawa et al., U.S. Patent No. 4,735,908 to Higashi et al., U.S. Patent No. 4,974,051 to Matloubian et al. and U.S. Patent No. 3,933,530 to Mueller et al. The Applicants respectfully traverse the rejection because the Official Action has not made a *prima facie* case of obviousness.

As stated in MPEP §§ 2142-2143.01, to establish a *prima facie* case of obviousness, three basic criteria must be met. First, there must be some suggestion or motivation, either in the references themselves or in the knowledge generally available to one of ordinary skill in the art, to modify the reference or to combine reference teachings. Second, there must be a reasonable expectation of success. Finally, the prior art reference (or references when combined) must teach or suggest all the claim

limitations. Obviousness can only be established by combining or modifying the teachings of the prior art to produce the claimed invention where there is some teaching, suggestion, or motivation to do so found either explicitly or implicitly in the references themselves or in the knowledge generally available to one of ordinary skill in the art. "The test for an implicit showing is what the combined teachings, knowledge of one of ordinary skill in the art, and the nature of the problem to be solved as a whole would have suggested to those of ordinary skill in the art." In re Kotzab, 217 F.3d 1365, 1370, 55 USPQ2d 1313, 1317 (Fed. Cir. 2000). See also In re Fine, 837 F.2d 1071, 5 USPQ2d 1596 (Fed. Cir. 1988); In re Jones, 958 F.2d 347, 21 USPQ2d 1941 (Fed. Cir. 1992).

The prior art, either alone or in combination, does not teach or suggest all the features of the independent claims. The independent claims recite an underlying insulating film formed on a glass substrate; and a pixel circuit and a driving circuit formed on said underlying insulating film; wherein said underlying insulating film contains halogen. In other words, the independent claims recite an underlying insulating film containing halogen formed between a transistor and a glass substrate.

The Official Action continues to concede that Misawa, Higashi and Matloubian do not teach "said underlying insulating film containing halogen" (page 3, Paper No. 20060122). The Official Action relies on Mueller to allegedly teach "a field effect transistor wherein a silicon dioxide layer (underlying insulating film) is formed on a silicon substrate ... [and] that the Cl ions make the silicon dioxide layer radiation hardened" (Id.). The Official Action further continues to assert that "it would have been obvious to one of ordinary skill in the art at the time of invention to have said underlying insulating film contain halogen in order to make the semiconductor device radiation hardened to reduce damage" (pages 3-4, Id.).

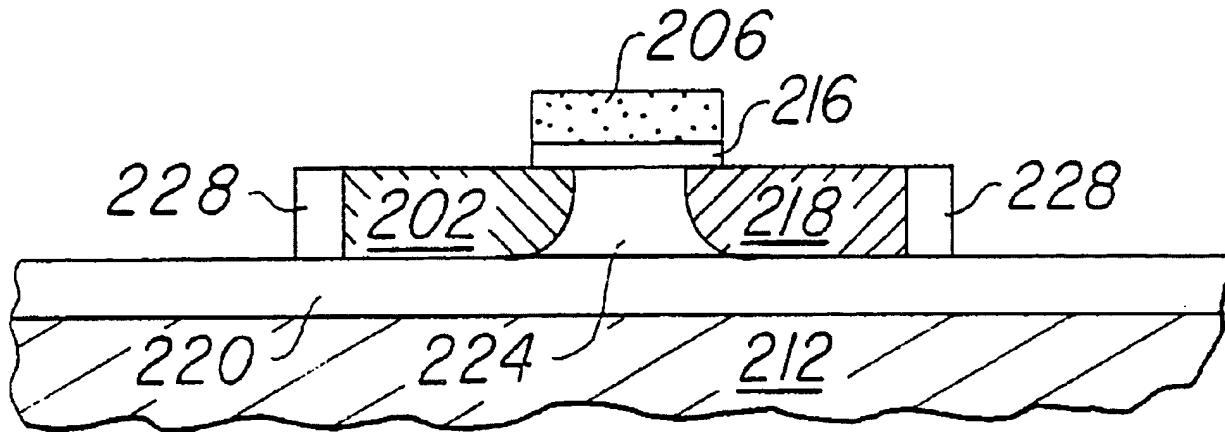


Fig. 6c

That is, the Official Action appears to assert that it would have been obvious to add halogen to the insulator layer 220 in Figure 6c of Matloubian (reproduced above).

Further, in the "Response to Arguments" section, the Official Action newly asserts that Mueller "does not state exclusively that the silicon oxide film must be used for the gate oxide of transistors" (page 8, Paper No. 20060122). The Applicants respectfully disagree and traverse the above-referenced assertions in the Official Action.

Mueller clearly teaches that "halide ions are implanted ... into the gate oxide of an IGFET" (column 2, lines 5-6); that "[a]n oxide (silicon oxide) layer is then grown thermally on the surface of the silicon body" (column 2, lines 22-24); and that "means are provided to grow an oxide layer, on the surface of a semiconductor material" (column 4, lines 15-16) (emphasis added). As such, it appears that the oxide layer of Mueller should be used as a gate insulating layer. Thus, the silicon dioxide layer of Mueller does not correspond to an underlying substrate.

In contrast, in the present invention, an underlying layer is formed between a transistor and a glass substrate in order to effectively prevent alkali metal atoms from getting into a semiconductor film from the glass substrate (see, for example, the specification at page 27, lines 3-34).

Therefore, Misawa, Higashi, Matloubian and Mueller, either alone or in combination, do not teach or suggest an underlying insulating film containing halogen formed between a transistor and a substrate.

Also, the independent claims have been amended to recite that an underlying film is formed on a glass substrate. An underlying film containing halogen has an advantage of preventing alkali metal atoms from getting into a semiconductor layer from a glass substrate (see page 27, lines 32-34 of the present specification). Misawa, Higashi, Matloubian and Mueller, either alone or in combination do not teach or suggest an underlying insulating film containing halogen formed on a glass substrate.

Since Misawa, Higashi, Matloubian and Mueller do not teach or suggest all the claim limitations, a *prima facie* case of obviousness cannot be maintained.

Furthermore, there is no suggestion or motivation, either in the references themselves or in the knowledge generally available to one of ordinary skill in the art, to modify Misawa, Higashi, Matloubian and Mueller or to combine reference teachings to achieve the claimed invention. MPEP § 2142 states that the examiner bears the initial burden of factually supporting any *prima facie* conclusion of obviousness. It is respectfully submitted that the Official Action has failed to carry this burden. While the Official Action relies on various teachings of the cited prior art to disclose aspects of the claimed invention and asserts that these aspects could be used together or modified in the manner asserted in the Official Action, it is submitted that the Official Action does not adequately set forth why one of skill in the art would combine the references to achieve the features of the present invention.

The test for obviousness is not whether the references "could have been" combined or modified as asserted in the Official Action, but rather whether the references should have been. As noted in MPEP § 2143.01, "The mere fact that references can be combined or modified does not render the resultant combination obvious unless the prior art also suggests the desirability of the combination." *In re Mills*, 916 F.2d 680, 16 USPQ2d 1430 (Fed. Cir. 1990) (emphasis in original). Thus, it

is respectfully submitted that the standard set forth in the Official Action is improper to support a finding of *prima facie* obviousness.

As noted above, the Official Action appears to assert that it would have been obvious to halogen to the insulator layer 220 in Figure 6c of Matloubian. The Applicants respectfully disagree and traverse the above-referenced assertions in the Official Action. Also as noted above, the silicon dioxide layer of Mueller does not correspond to an underlying substrate. One of ordinary skill in the art at the time of the present invention would not have applied the teachings of Mueller, which relate to a gate insulator of a transistor (column 4, lines 5-40), to the insulator layer 220 in Figure 6c of Matloubian, which appears to be formed between substrate 212 and a transistor. In other words, there is no suggestion or motivation in the prior art that would have taught or suggested that it would have been obvious to apply a gate insulator to an insulator layer between a substrate and a transistor.

Also, the Official Action asserts that Mueller's silicon oxide film "would clearly benefit the insulator 220 of [Matloubian]" (pages 8-9, Paper No. 20060122; the Applicant notes that it appears that the Official Action intended "Matloubian" here instead of "Higashi" as shown at page 9 of the Office Action). However, as noted above, the silicon oxide layer of Mueller should be used as the gate insulating film and does not correspond to the insulator 220 formed on a substrate. The prior art does not recognize an advantage that would result from forming an underlying layer between a transistor and a substrate. Whereas, the present inventors have found that forming an underlying layer between a transistor and a substrate is effective in preventing alkali metal atoms from getting into a semiconductor film from a glass substrate. Therefore, the Applicant believes that one of ordinary skill in the art at the time of the present invention would not have been motivated to apply the teaching of Mueller to the insulator layer 220 of Matloubian. In view of the above, there is no suggestion or motivation in the prior art that would have taught or suggested that it would have been obvious to apply a gate insulator to an underlying insulating film on a glass substrate.

Further, it is not clear why one of ordinary skill in the art who was concerned with the alleged motivation, i.e. making a semiconductor device radiation hardened to reduce damage (page 3, Paper No. 20060122) would not have simply practiced Mueller alone. It is unclear why it would have been desirable to combine Misawa, Higashi, Matloubian and Mueller at the time of the present invention. Therefore, the Official Action has not shown sufficient motivation in Misawa, Higashi and Matloubian or Mueller to teach or suggest that the references could or should be combined.

Therefore, the Applicants respectfully submit that the Official Action has not provided a proper suggestion or motivation, either in the references themselves or in the knowledge generally available to one of ordinary skill in the art, to modify Misawa, Higashi, Matloubian and Mueller or to combine reference teachings to achieve the claimed invention.

For the reasons stated above, the Official Action has not formed a proper *prima facie* case of obviousness. Accordingly, reconsideration and withdrawal of the rejections under 35 U.S.C. § 103(a) are in order and respectfully requested.

Paragraph 4 of the Official Action rejects dependent claims 9, 18, 31, 44 and 57 as obvious based on the combination of Misawa, Higashi, Matloubian, Mueller and U.S. Patent No. 5,153,142 to Hsieh.

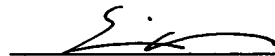
Please incorporate the arguments above with respect to the deficiencies in Misawa, Higashi, Matloubian and Mueller. Hsieh does not cure the deficiencies in Misawa, Higashi, Matloubian and Mueller. The Official Action relies on Hsieh to allegedly teach a pixel electrode connected to a thin film transistor via a conductive film (page 8, Paper No. 20060122). However, Misawa, Higashi, Matloubian, Mueller and Hsieh, either alone or in combination, do not teach or suggest an underlying insulating film containing halogen formed between a transistor and a substrate.

Since Misawa, Higashi, Matloubian, Mueller and Hsieh do not teach or suggest all the claim limitations, a *prima facie* case of obviousness cannot be maintained.

Accordingly, reconsideration and withdrawal of the rejections under 35 U.S.C. § 103(a) are in order and respectfully requested.

Should the Examiner believe that anything further would be desirable to place this application in better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number listed below.

Respectfully submitted,



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